



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Hiroshi HASHIMOTO et al. Group Art Unit: 2814

Application Number: 10/083,533 Examiner: Thao X. Le

Filed: February 27, 2002 Confirmation Number: 6400

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A

SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD

CHARACTERISTIC

Attorney Docket Number: 020244

Customer Number: 38834

AMENDMENT UNDER 37 C.F.R. §1.111

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 December 29, 2006

Sir:

This paper is filed in response to the Office Action dated September 29, 2006.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 14 of this paper.